

# Design Peculiarities of Digital and I/O Standard Cell Libraries Based on FinFET Technologies

## Abstract:

Circuit, physical design, characterization and verification peculiarities of Digital and I/O Standard Cell Libraries are described, which are conditioned by the deployment of FinFET Technologies. It is shown how the peculiarities effect the cell list, characterization corners, circuits, layouts of library cells. 14nm low power digital and standard cell library development experience as well as their key characteristics are presented.